

# 4 Modelling Issues

## What do you measure?

- ▶ **Memory requirement**
- ▶ Running time
- ▶ Number of comparisons
- ▶ Number of multiplications
- ▶ Number of hard-disc accesses
- ▶ Program size
- ▶ Power consumption
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## How do you measure?

- ▶ Implementing and testing on representative inputs
  - ▶ How do you choose your inputs?
  - ▶ May be very time-consuming.
  - ▶ Very reliable results if done correctly.
  - ▶ Results only hold for a specific machine and for a specific set of inputs.
- ▶ Theoretical analysis in a specific **model of computation**.
  - ▶ Gives a **lower bound** like "this algorithm always runs in  $O(n \log n)$  time".
  - ▶ Typically focuses on the **number of comparisons**.
  - ▶ Can this lower bound also apply to comparison-based sorting algorithms? needs at least  $\Omega(n \log n)$  comparisons in the worst case.

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Quick question: Is the following algorithm always runs in  $O(n \log n)$  time?

Selection Sort

Typical focus on the

Can this lower bound be any comparison-based sorting algorithm needs at least  $\Omega(n \log n)$  comparisons in the worst case.

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Quick question: How many steps does this algorithm always take?

One billion.

Typically,  $n^2$  comparisons.

Can this lower bound be any computer-*independent* sorting algorithm needs at least  $\Omega(n \log n)$  comparisons in the worst case.

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Why not?

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Quick question: How many algorithms always runs in  $O(n^2)$  time?

Answer: None.

Why not? Because the best comparison-based sorting algorithm needs at least  $\Omega(n \log n)$  comparisons in the worst case.

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### Input length

The theoretical bounds are usually given by a function  $f : \mathbb{N} \rightarrow \mathbb{N}$  that maps the **input length** to the running time (or storage space, comparisons, multiplications, program size etc.).

The **input length** may e.g. be

the size of the input (number of bits)

the number of arguments

the number of bits of the input (number of bits of the arguments)

the number of bits of the output (number of bits of the result)

the number of operations

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## How to measure performance

1. Calculate running time and storage space etc. on a simplified, idealized model of computation, e.g. Random Access Machine (RAM), Turing Machine (TM), . . .
2. Calculate number of certain basic operations: comparisons, multiplications, harddisc accesses, . . .

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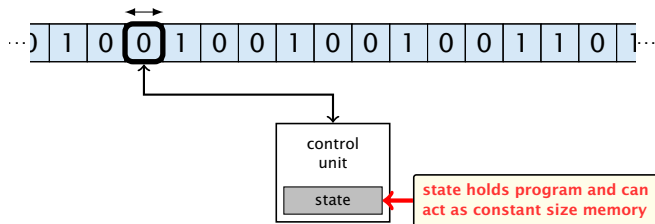
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# Turing Machine

- ▶ Very simple model of computation.
- ▶ Only the “current” memory location can be altered.
- ▶ Very good model for discussing computability, or polynomial vs. exponential time.
- ▶ Some simple problems like recognizing whether input is of the form  $x^x$ , where  $x$  is a string, have quadratic lower bound.

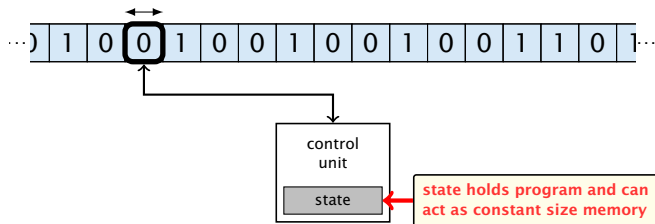
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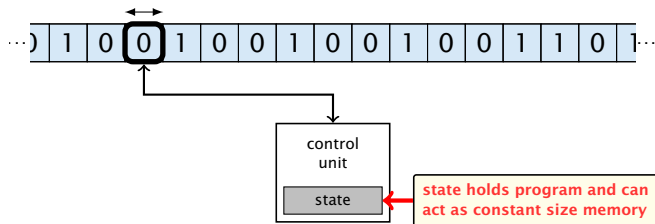
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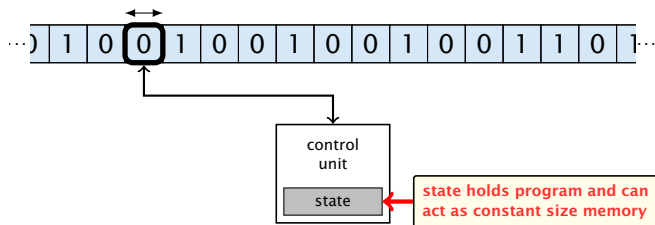
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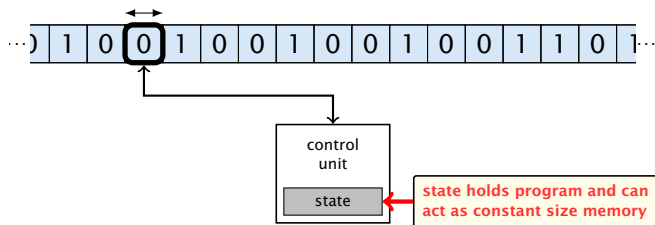
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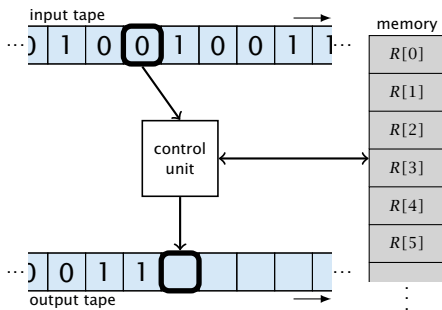
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# Random Access Machine (RAM)

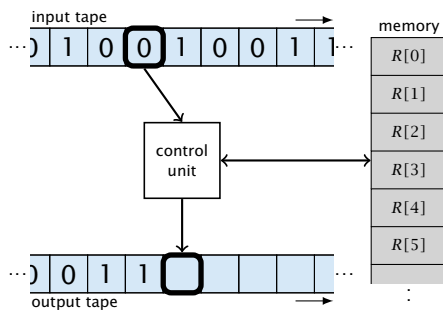
- ▶ Input tape and output tape (sequences of zeros and ones; unbounded length).
- ▶ Memory unit: infinite but countable number of registers  $R[0], R[1], R[2], \dots$
- ▶ Registers hold integers.
- ▶ Indirect addressing.





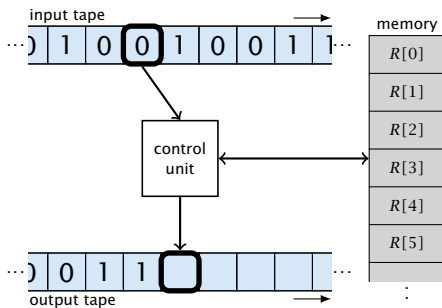
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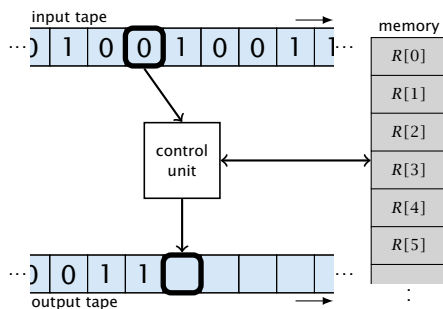
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## Operations

- ▶ input operations (input tape  $\rightarrow R[i]$ )
  - ▶ READ  $i$
- ▶ output operations ( $R[i] \rightarrow$  output tape)
- ▶ register-register transfers
  - $R[i] \leftarrow R[j]$
  - $R[j] \leftarrow R[i]$
- ▶ indirect addressing
  - $R[i] \leftarrow R[R[j]]$   
loads the content of the  $j$ -th register into the  $i$ -th register
  - $R[R[i]] \leftarrow R[j]$   
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  - ▶ `jump  $x$`   
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sets instruction counter to  $x$ ;  
reads the next operation to perform from register  $R[x]$
  - ▶ `jumpz  $x$   $R[i]$`   
jump to  $x$  if  $R[i] = 0$   
if not the instruction counter is increased by 1;
  - ▶ `jumpi  $i$`   
jump to  $R[i]$  (indirect jump);
- ▶ arithmetic instructions:  $+$ ,  $-$ ,  $\times$ ,  $/$   
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# Model of Computation

- ▶ **uniform** cost model

Every operation takes time 1.

- ▶ **logarithmic** cost model

The cost depends on the content of memory cells:

- ▶ The time for a step is equal to the largest operand involved.

- ▶ The amount of space of a register is equal to the length of

- ▶ the largest value ever stored in it.

**Bounded word RAM model:** cost is uniform but the largest value stored in a register may not exceed  $w$ , where usually  $w = \log_2 n$ .

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Usually easy to analyze, but not very meaningful.

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